

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Subhasish Mitra et al.	§	Art Unit: 2117
	§	
Serial No.: 10/656,013	§	Examiner: Phung M. Chung
	§	
Filed: September 5, 2003	§	Docket: ITL.0971US
	§	P16171
For: Compacting Circuit Responses	§	
	§	Assignee: Intel Corporation
	§	

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

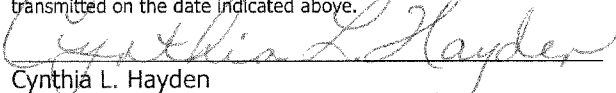
REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed November 14, 2008, please amend the above-referenced patent application as follows:

Date of Deposit: February 4, 2009

I hereby certify that this correspondence is being electronically transmitted on the date indicated above.


Cynthia L. Hayden